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CONFIRMATION NO. 7356

SERIAL NUMBER 10/815,083	FILING DATE 03/31/2004  RULE	CLASS <del>257</del> 438	GROUP ART UNIT 2891	ATTORNEY DOCKET NO. 97-1363.5
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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 09/961,646 09/25/2001 PAT 6,833,613  
 which is a DIV of 09/385,606 08/30/1999 PAT 6,294,837  
 which is a DIV of 08/993,965 12/18/1997 PAT 6,107,109

LT

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

(None) LT

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 06/08/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY ID	SHEETS DRAWING 6	TOTAL CLAIMS 31	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature <i>Shailman</i>	Initials <i>LT</i>		

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## TITLE

Method for fabricating semiconductor components by forming conductive members using solder

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees ( Filing )
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